

Research Article

Realization of Discrete Cosine Transform Using Cyclic Convolutions with Generalized Approach

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Abstract

A general solution is proposed to realize the discrete cosine transform of any length via cyclic convolutions in this paper. This algorithm is not optimal in minimizing any measure of computational complexity, but it involves some regular forms that are most suitable for the realization using technologies and structures which are well suited for doing convolutions, such as the distributed arithmetic and the systolic array. On the other hand, this algorithm is much more flexible than any available DCT algorithm as it can be applied to realize DCT/IDCT with any length.

Keywords: Discrete Cosine Transform (DCT), Inverse Discrete Cosine Transform (IDCT), Convolver, Convolution, Discrete Fourier Transform (DFT), Algorithm, Series Expansions, and Hardware Realization.

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Introduction

It is well known that one can use chirp-z transform to realize a DFT with any length through convolutions. This algorithm is not optimal in minimizing any measure of computational complexity, but it is useful and efficient in the realization of DFT with variable length as it is much more flexible compared with other DFT algorithms for certain applications.

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It is obvious that similar advantages can be obtained for the realization of the DCT, if there exists a similar general solution to the DCT [1]. Unfortunately, one cannot apply the chirp-z transform to obtain DCT directly as in the DFT case, since the kernels of the two transforms are completely different. Possible solution can be achieved by firstly inverting the DCT into a DFT [2, 3] and then applying the chirp-z transform directly. However, this approach involves complex number operations and seems a little bit circuitous. Hence, it is necessary for us to look for a more straight-forward solution which involves real operations solely.

The aim of this paper is to suggest such a general solution for the realization of the DCT and to propose a structure to ease the implementation of this solution. In the sections that follow in this paper, the basic algorithm employed will be presented, relevant deductions will be made, an example given with solution method outlined, the hardware implementation of the DCT is suggested and the concluding remarks are made.

Basic Algorithm

The definition of the DCT on a sequence $\{y(i):i=0,1,...N-1\}$ is given as:

$$Y(k) = \sum_{i=0}^{N-1} y(i) \cos \left[\frac{\pi(2i+1)k}{2N} \right]; \quad \text{for } k = 0,1,...N-1 \quad (1)$$

In ref. [4], we have shown that by defining another sequence $\{x(i):i=0,1,...N-1\}$ as

$$\begin{aligned} x(N-1) &= y(N-1) \\ x(i) &= y(i) - x(i+1) \end{aligned} \quad \text{for } i = 0,1,...N-2 \quad (2)$$

We have:

$$Y(k) = \{2T(k) + x(0)\} \cos\left(\frac{k\pi}{2N}\right); \quad \text{for } k = 0,1,...N-1 \quad (3)$$

$$\text{Where } T(k) = \sum_{i=0}^{N-1} x(i) \cos \frac{ik\pi}{N} \quad \text{for } k = 0,1,... N-1 \quad (4)$$

Instead of realizing Eqn. 4 directly, we can realize another sequence $\{X(k)\}$ defined below and then compute $\{T(k)\}$ from $\{X(k)\}$ indirectly. First of all, we have to define the sequence $\{X(k)\}$:

$$X(k) = \sum_{i=0}^{N-1} x(i) \cos \frac{\pi}{2N} (k^2 - 2ik) \quad \text{for any integer } k. \quad (5)$$

Obviously, we have:

$$X(-k) = X(2N - k) = \sum_{i=1}^{N-1} x(i) \cos \frac{\pi}{2N} (k^2 - 2ik) \quad \text{for any integer } k. \quad (6)$$

Then, $\{T(k)\}$ can be obtained by:

$$T(k) = \frac{1}{2} (X(k) + X(2N-k)) \sec \frac{k^2 \pi}{2N} ; \quad \text{for } k = 0, 1, \dots, N-1 \quad (7)$$

In particular, $\{X(k)\}$ can be rewritten as:

$$X(k) = \sum_{i=1}^{N-1} h(i) \cos \left[\frac{(i-k)^2 \pi}{2N} \right] + \sum_{i=1}^{N-1} g(i) \sin \left[\frac{(i-k)^2 \pi}{2N} \right] \quad \text{for any integer } k \quad (8)$$

$$\text{Where } h(i) = x(i) \cos \frac{i^2 \pi}{2N} \quad \text{for } i = 1, 2, \dots, N-1 \quad (9)$$

$$g(i) = x(i) \sin \frac{i^2 \pi}{2N} \quad \text{for } i = 1, 2, \dots, N-1 \quad (10)$$

To obtain the DCT of the sequence $\{y(i)\}$, we have to obtain the sequence $\{T(k): k = 0, 1, \dots, N-1\}$. That means we have to compute $\{X(k): k = 0, 1, \dots, N-1, N+1, N+2, \dots, 2N\}$. However, as $T(0) = \sum_{i=1}^{N-1} x(i)$, all we need to do is to calculate $\{X(k): k = 1, 2, \dots, N-1, N+1, N+2, \dots, 2N-1\}$. In other words, from Eqn. 8, we have to compute the following two-linear convolutions:

$$G(k) = \sum_{i=1}^{N-1} g(i) \sin \left[\frac{\pi}{2N} (i - k)^2 \right] \quad \text{for } k \in \{1, 2, \dots, 2N-1\} \setminus \{N\} \quad (11)$$

$$H(k) = \sum_{i=1}^{N-1} h(i) \cos \left[\frac{\pi}{2N} (i - k)^2 \right] \quad \text{for } k \in \{1, 2, \dots, 2N-1\} \setminus \{N\} \quad (12)$$

In such case, one can realize a DCT with any length N – by using two N -length linear convolutions, while the overheads are at most $4(N-1)$ multiplications.

Further Deduction

Sometimes it is preferable to compute the DCT by cyclic convolutions instead of linear convolutions due to the efficiency of the realization especially when dedicated hardware structure can be used. In such case, we can make some modifications on eqn. 7 such that we can make use of cyclic convolutions. Obviously, for eqns. 7-12, we have.

$$T(k) = \frac{1}{2} [H(k) + H(2N-k) + G(k) + G(2N-k)] \sec \left[\frac{k^2 \pi}{2N} \right] \quad \text{for } k = 0, 1, \dots, N-1 \quad (13)$$

As $T(0) = \sum_{i=0}^{N-1} x(i)$, we can compute this item directly through simple addition. For other items of the sequence $\{T(k)\}$, we can first compute two sequences, $\{H(k) + H(2N-k)\}$ and $\{G(k) + G(2N-k)\}$. That is, for the sequence $(H(k) + H(2N-k))$, we compute.

$$H(k) + H(2N-k) = \sum_{i=1}^{2N-1} h'(i) \cos \left[\frac{(i-k)^2 \pi}{2N} \right] \quad \text{for } k = 1, 2, \dots, N-1 \quad (14)$$

Where $\{h'(i): i = 0, 1, \dots, 2N-1\}$ is defined as

$$h'(i) = \begin{cases} h(i) & \text{if } i=1, 2, \dots, N-1 \\ 0 & \text{if } i=N \\ h(2N-i) & \text{if } i=N+1, N+2, \dots, 2N-1 \end{cases} \quad (15)$$

Similarly, by defining another sequence $\{g'(i): i=0, 1, \dots, 2N-1\}$ as

$$g'(i) = \begin{cases} g(i) & \text{if } i=1, 2, \dots, N-1 \\ 0 & \text{if } i=N \\ g(2N-i) & \text{if } i=N+1, N+2, \dots, 2N-1 \end{cases} \quad (16)$$

We have
$$G(k) + G(2N-k) = \sum_{i=1}^{2N-1} g'(i) \sin \left[\frac{(i-k)^2 \pi}{2N} \right] \quad \text{for } k = 1, 2, \dots, N-1 \quad (17)$$

Note that both $\{\cos[n^2 \frac{\pi}{2N}]: n \in \mathbb{Z}\}$ and $\{\sin[n^2 \frac{\pi}{2N}]: n \in \mathbb{Z}\}$

(where $\mathbb{Z}$ is the set of integers) are cyclic with a period of $[2N]$, that means equations 14 and 17 are both in cyclic convolution form after appending $h'(0)=0$ and $g'(0)=0$ to sequences $\{h'(i)\}$ and $\{g'(i)\}$, respectively. Note that it is necessary to compute the whole cyclic convolution as only $N-1$ items of $\{G(k) + G(2N-k)\}$ approach can save $2(N-1)$ additions in realizing eqn. 7 compared with the approach using linear convolutions.

Example

Let us use a 6-point DCT to clarify our proposal. Suppose the input sequence is given as $\{y(i): i=0, 1, \dots, 5\}$, Then, obviously, we have $Y(0) = \sum_{i=0}^5 y(i)$. For the other DCT coefficients, we first compute the sequence $\{x(i)\}$ from $\{y(i)\}$ with eqn. 2:

$$\begin{aligned} x(5) &= y(5) \\ x(4) &= y(4) - y(5) \\ x(3) &= y(3) - y(4) + y(5) \end{aligned}$$

$$x(2) = y(2)-y(3)+y(4)-y(5)$$

$$x(1) = y(1)-y(2)+y(3)-y(4)+y(5)$$

$$x(0) = y(0)-y(1)+y(2)-y(3)+y(4)-y(5)$$

Then, based on eqns. 9,10,14-17, we can realize the following two cyclic convolutions to get sequences $\{G(k) + G(12-k)\}$ and $\{H(k) + H(12-k)\}$:

$$\begin{bmatrix} H(1) + H(11) \\ H(2) + H(10) \\ H(3) + H(9) \\ H(4) + H(8) \\ H(5) + H(7) \end{bmatrix}$$

$$\begin{bmatrix} c(1) & c(0) & c(1) & c(4) & c(9) & c(16) & c(1) & c(12) & c(1) & c(16) & c(9) & c(4) \\ c(4) & c(1) & c(0) & c(1) & c(4) & c(9) & c(16) & c(1) & c(12) & c(1) & c(16) & c(9) \\ c(9) & c(4) & c(1) & c(0) & c(1) & c(4) & c(9) & c(16) & c(1) & c(12) & c(1) & c(16) \\ c(16) & c(9) & c(4) & c(1) & c(0) & c(1) & c(4) & c(9) & c(16) & c(1) & c(12) & c(1) \\ c(1) & c(16) & c(9) & c(4) & c(1) & c(0) & c(1) & c(4) & c(9) & c(16) & c(1) & c(12) \end{bmatrix}$$

$$[0 \ h(1) \ h(2) \ h(3) \ h(4) \ h(5) \ 0 \ h(5) \ h(4) \ h(3) \ h(2) \ h(1)]^T$$

$$\begin{bmatrix} G(1) + G(11) \\ G(2) + G(10) \\ G(3) + G(9) \\ G(4) + G(8) \\ G(5) + G(7) \end{bmatrix}$$

$$\begin{bmatrix} s(1) & s(0) & s(1) & s(4) & s(9) & s(16) & s(1) & s(12) & s(1) & s(16) & s(9) & s(4) \\ s(4) & s(1) & s(0) & s(1) & s(4) & s(9) & s(16) & s(1) & s(12) & s(1) & s(16) & s(9) \\ s(9) & s(4) & s(1) & s(0) & s(1) & s(4) & s(9) & s(16) & s(1) & s(12) & s(1) & s(16) \\ s(16) & s(9) & s(4) & s(1) & s(0) & s(1) & s(4) & s(9) & s(16) & s(1) & s(12) & s(1) \\ s(1) & s(16) & s(9) & s(4) & s(1) & s(0) & s(1) & s(4) & s(9) & s(16) & s(1) & s(12) \\ [0 \ g(1) \ g(2) \ g(3) \ g(4) \ g(5) \ 0 \ g(5) \ g(4) \ g(3) \ g(2) \ g(1)]^T \end{bmatrix}$$

Where $c(n) = \cos \left[n \frac{x}{12} \right]$, $s(n) = \sin \left[n \frac{x}{12} \right]$, $h(n) = x(n)c(n^2)$
and $g(n) = x(n)s(n^2)$. From eqn. 13:

$$\begin{bmatrix} T(1)1/2c(1) \times (G(1)+H(1)+G(11)+H(11)) \\ T(2)1/2c(4) \times (G(2)+H(2)+G(10)+H(10)) \\ T(3)1/2c(9) \times (G(3)+H(3)+G(9)+H(9)) \\ T(4)1/2c(16) \times (G(4)+H(4)+G(8)+H(8)) \\ T(5)1/2c(25) \times (G(5)+H(5)+G(7)+H(7)) \end{bmatrix}$$

We have, and finally we can obtain, the other DCT coefficients through the realization of eqn. 3:

$$\begin{bmatrix} Y(1)(2T(1) + x(0)) \times c(1) \\ Y(2)(2T(2) + x(0)) \times c(2) \\ Y(3)(2T(3) + x(0)) \times c(3) \\ Y(4)(2T(4) + x(0)) \times c(4) \\ Y(5)(2T(5) + x(0)) \times c(5) \end{bmatrix}$$

If N is small, the overhead of $4(N-1)$ multiplications will dominate the computational effort required and therefore the implementation of this algorithm may be inefficient. Nevertheless, when N gets larger, this overhead will become less and finally negligible. In such case, the implementation of this algorithm can be very efficient as it mainly involves two cyclic convolutions only, which require no complicated data management and routing algorithm.

Inverse DCT

The inverse Discrete Cosine Transform (IDCT) of data $[Y(k):k = 0,1\dots N-1]$ is given by the following:

$$y(i) = \sum_{k=0}^{N-1} Y(k) \cos \left[\frac{\pi(2i+1)k}{2N} \right] \quad \text{for } i=0,1\dots,N-1 \quad (18)$$

Obviously, if one can rewrite eqn.18 such that it can be realized through the formulation in the form of eqn.4, eqn.18 can also be realized using convolutions. In fact, this can be readily achieved by defining another sequence.

$$y'(i) = y(i) + y(i-1) = \sum_{k=1}^{N-1} \{2Y(k) \cos \frac{k\pi}{2N}\} \cos \frac{ik\pi}{N} + 2Y(0) \quad \text{for } i=0,1\dots,N-1 \quad (19)$$

Where $y(-1)$ is defined to be equal to $y(0)$.

We have $Y(0) = 2y(0)$ and thereof $\{y(i):i=0,1,\dots,N-1\}$ can be obtained from $\{y(i):i=0,1,\dots,N-1\}$ through $N-1$ additions. Note that eqn.19 is in the form of eqn.4.

Hence by applying the same technique used in above sections, one can realize an ISCT with two linear convolutions or two cyclic convolutions.

Hardware Implementation

This algorithm suggests a straight-forward approach to implement a unified DCT/IDCT hardware system. Figures 1 and 2 show the flow diagrams for DCT and IDCT respectively. One can see that the switch from DCT to IDCT or vice versa of such a system implies the change of the order of the two hardware structures, namely, the recursive adder and the convolution module only. Note that the multiplier in the output stage is disabled in IDCT mode as the multiplication of the $c(k)$ value is embedded into the adder to reduce the computation time, but also reduces the computational error arose during multiplications.

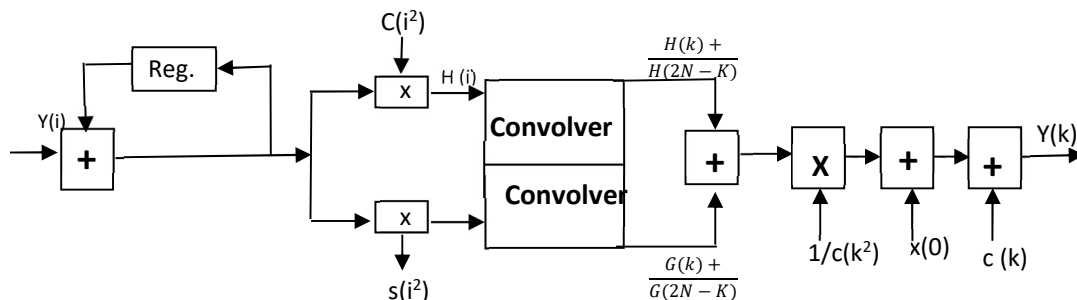
The convolution module shown in figures 1 and 2 include two convolves. The distributed arithmetic technique [5] is applied here due to its simple structure. From eqns. 14 and 17, we know that we have to realize two $2N$ -point cyclic convolutions. That means one has to construct two tables for the realization of the two equations I, in which the distributed arithmetic technique is applied.

In particular, we need a table of

$$\sum_{n=0}^{2n-1} b_n \cos \left[\frac{n^2 \pi}{2N} \right] : b_n \in \{0,1\}$$

for realizing eqn. 14 and another table of

$$\sum_{n=0}^{2n-1} b_n \cos \left[\frac{n^2 \pi}{2N} \right] : b_n \in \{0,1\}.$$



For realizing eqn.17, hence, the basic size of each table will be 2^{2N} words. However, one must make a further simplification to reduce the table size as required:

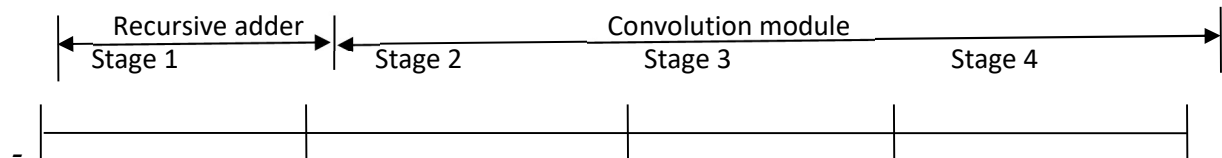


Fig. 1: Flow diagram for the implementation of the DCT

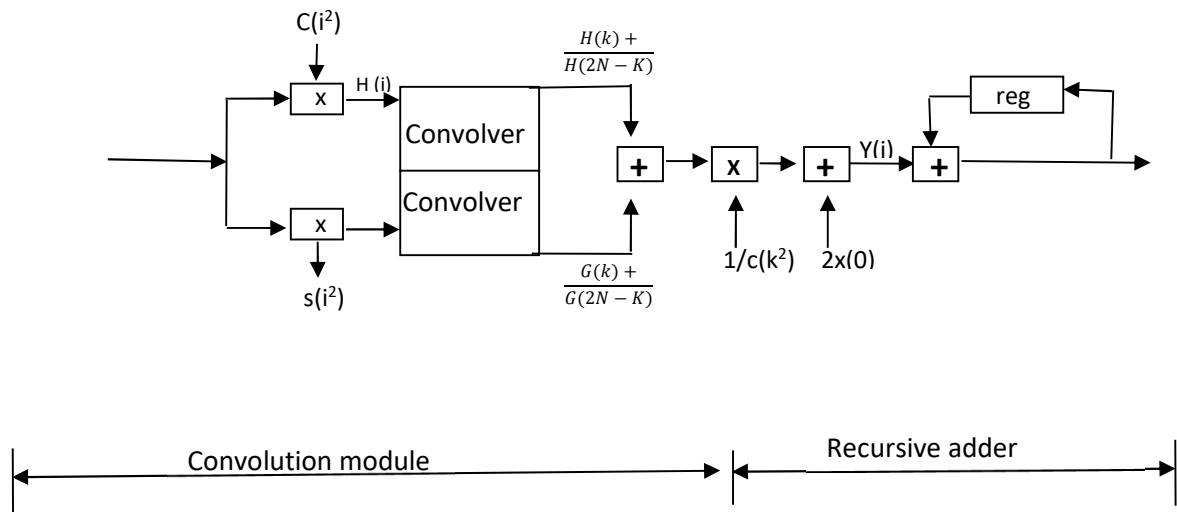


Fig. 2: Flow diagram for the implementation of the IDCT.

By substituting $i=j + K$ into eqn. 1, we have:

$$H(k) + H(2N - k) = \sum_{j=1}^{2N-1-K} H'(j + K) \cos \left[\frac{j\pi}{2N} \right] \quad \text{For } K = 1, 2, \dots, N-1 \quad (20)$$

As $h'(0) = 0$, eqn. 20 can then be rewritten as:

$$H(k) + H(2N - k) \cos \left[\frac{N\pi}{2} \right] + \sum_{j=1}^{N-1} h''(j, k) \cos \left[\frac{j\pi}{2N} \right] \quad \text{for } K = 1, 2, \dots, N-1 \quad (21)$$

$$\text{Where } h''(j, K) = \begin{cases} \frac{h'(j+K) + h'(K-j)}{h'(j+K) + h'(2N-j+K)} & \text{if } 0 < j \leq k \\ & \text{if } k < j < n \end{cases} \quad (22)$$

To realize eqn. 21, only a table of $\sum_{n=0}^{N-1} b_n \cos \left[\frac{j\pi}{2N} \right]$:

$b_n \in \{0, 1\}$ is required in such a case, where the basic table size can be reduced to 2^{n-1} words. Storage elements required can then be reduced to one 2^{n+1} -th of the original approach. Similar simplification can be used to realize eqn. 17.

Figure 3 shows the structure of such a convolver. In conventional distributed arithmetic structure, the address for accessing the ROM table is generated from a circular buffer [5] in the initial state, data are loaded into the circular buffer. Then, a specific bit of each datum will be selected and combined with appropriate bits from other data to form an address to access the ROM table. Fetched data are shifted and accumulated to form partial result until the final result comes out.

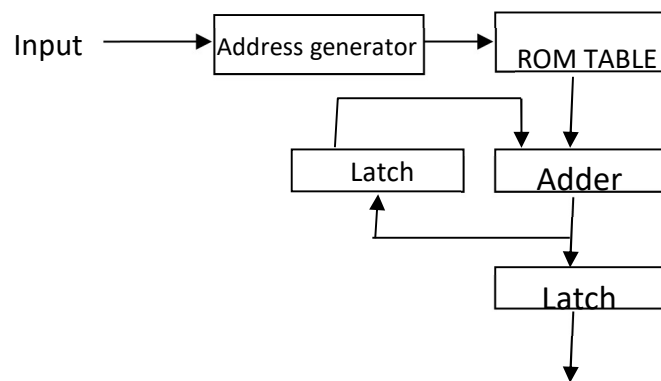


Fig 3a: Convolver constructed with distributed

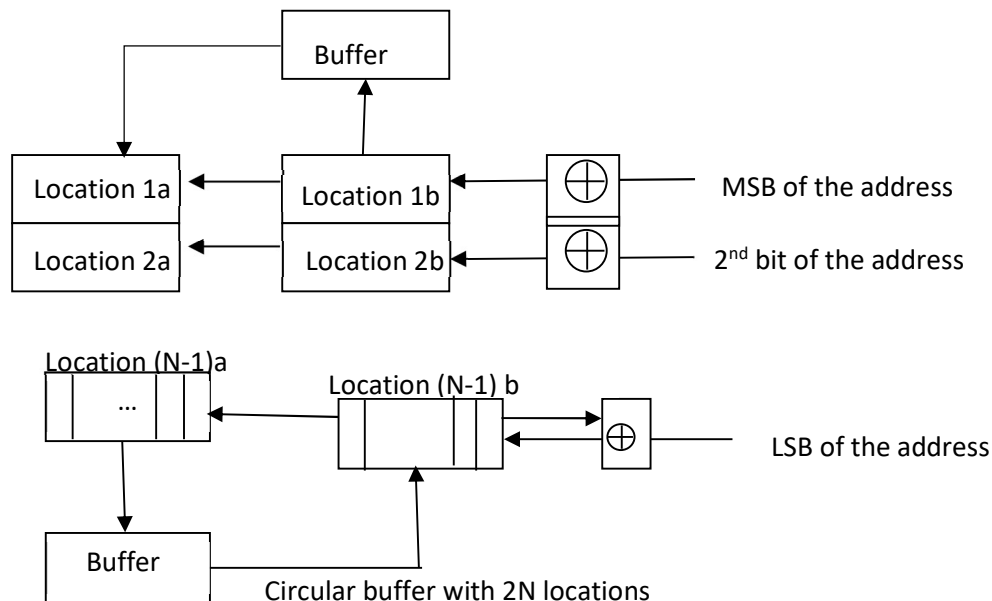


Fig 3b: Modified circular buffer used as the address generator in the convolver.

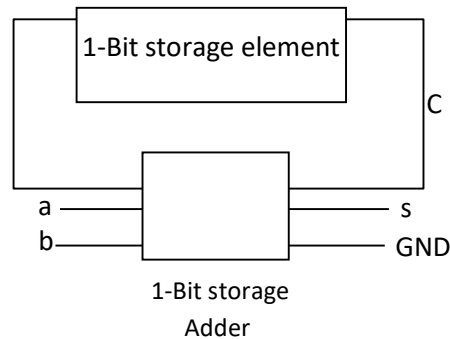


Fig. 3b: Continued

partial result until the final result comes out. Whenever an output is obtained, all data in the circular buffer will circulate by one step and the above procedure will be repeated until all outputs are obtained. For the structure proposed, the address is generated from a modified circular buffer as shown in figure 3b instead of a typical circular buffer.

Numerical Stability

From eqn.7, we know that the present approach involves the multiplication of the sec ($k^2\pi/2N$) term for each value of k . In particular, as $\cos(k^2\pi/2N) = 0$ implies $(k^2)_{2N} = N$ ($0 < k < N$) and vice versa, it means one has to face this problem can be easily resolved without too much additional effort especially when the distributed arithmetic technique is applied.

Obviously, if $\cos \frac{k^2 0 \pi}{2N} = 0$, we have $\sin \frac{k^2 0 \pi}{2N} = 1$ or -1 .

Moreover, we have

$$\sum_{i=1}^{2N-1} h'(i) \sin \frac{(i-ko)^2 \pi}{2N} - \sum_{i=1}^{2N-1} g'(i) \cos \frac{(i-ko)^2 \pi}{2N} = 2 \sin \frac{k^2 0 \pi}{2N} T(k)$$

Hence, by making a small modification, one can still make use of the structure proposed in the previous sections. In fact, only multiplexers are required to add to the convolver such that one can swap the data table fetched during the computation of the value of $T(k_0)$ as shown in figure 4. In general, the two convolves perform the same operations as those described in the circular buffer containing $\{h(i)\}$ generates the address to access the table of $\sum_{n=1}^{N-1} b_n \cos \left[\frac{n^2 \pi}{2N} \right] : b_n \in \{0,1\}$ while the circular buffer containing $\{g(i)\}$ generates the address to access the table of $\sum_{n=1}^{N-1} b_n \sin \left[\frac{n^2 \pi}{2N} \right] : b_n \in \{0,1\}$.

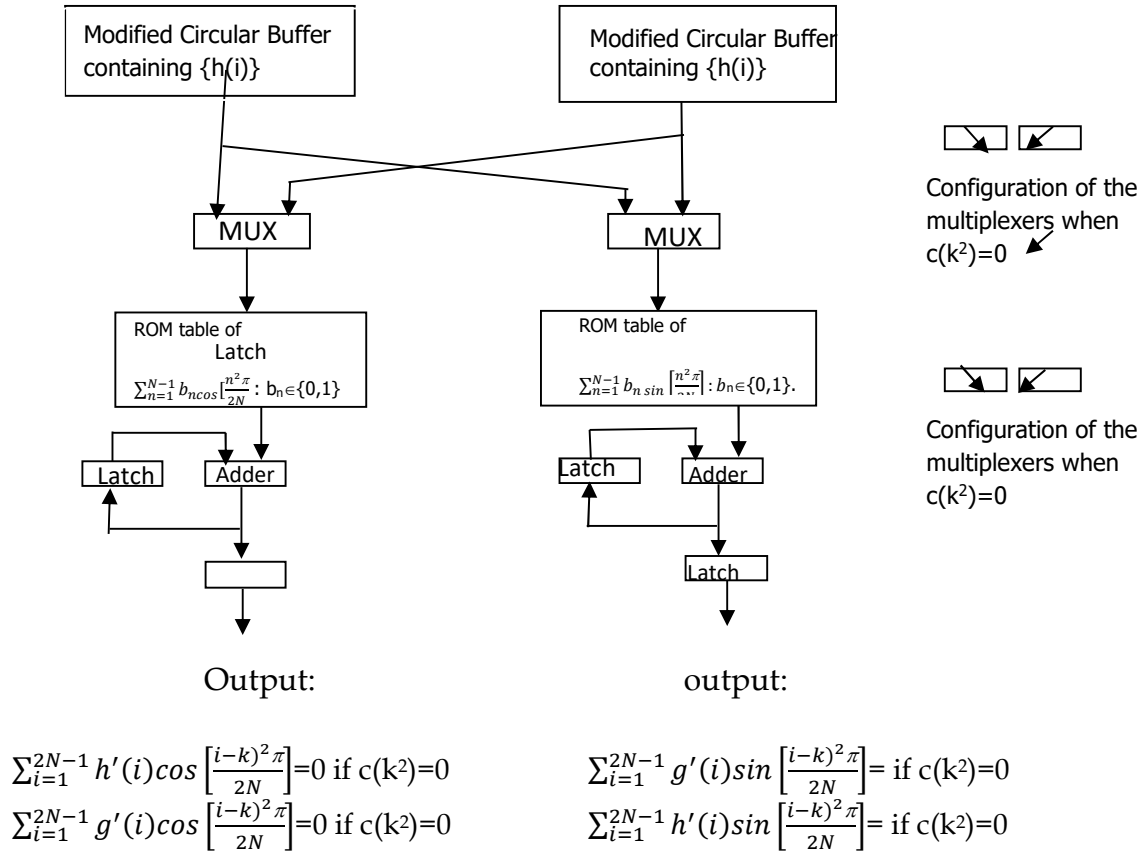


Fig. 4: Convolvers swapping data table to avoid numerical instability by using multiplexers.

$\{g(i)\}$ generates the address to access the table of $\sum_{n=1}^{N-1} b_n \sin \left[\frac{n^2 \pi}{2N} \right] : b_n \in \{0,1\}$. In other words, no special treatment is required for this normal case. However, for the computation of the value of $T(k_0)$, we swap the data table such that the address generated from the circular buffer containing $\{g(i)\}$ and the circular buffer containing $\{h(i)\}$ are respectively used to access the table of $\sum_{n=1}^{N-1} b_{ncos} \lfloor \frac{n^2 \pi}{2N} \rfloor : b_n \in \{0,1\}$ and the table of $\sum_{n=1}^{N-1} b_n \sin \left[\frac{n^2 \pi}{2N} \right] : b_n \in \{0,1\}$ instead. Then the outputs of the two convolvers will be $\sum_{i=1}^{2N-1} h'(i) \sin \frac{(i-k_0)^2 \pi}{2N}$ and $\sum_{i=1}^{2N-1} g'(i) \cos \frac{(i-k_0)^2 \pi}{2N}$ in this cycle. For the present design, the value of $1/c(k_0^2)$ is assigned to be $\sin(k_0^2 \pi / 2N)$ ($=1$ or -1) and the value of $\sum_{i=1}^{2N-1} g'(i) \cos \frac{(i-k_0)^2 \pi}{2N}$ is negated before being fed into stage 4 of the system. The same implementation structure as shown in figure 1 can then be used to obtain results of these special cases. Note that this trick can also be used to eliminate numerical instability in the case of $\cos(k^2 \pi / 2N) = 0$. Similar modification can be applied to the realization of the IDCT.

Conclusion

In this paper, we propose a general solution for the realization of the discrete cosine transform and the inverse discrete cosine transform. This algorithm converts the DCT/IDCT into convolution form such that one can easily implement it with technologies that are well suited for doing convolutions. This is an efficient and effective approach as it can avoid complicated data routing and data management. For example, there is no address generation problem and a simple pipeline structure can be applied to achieve parallel processing. Compared with conventional algorithms, this algorithm is much more flexible as it can be applied to realize DCT/IDCT with any length. On the other hand, this algorithm suggests an efficient approach to realize an unified hardware for the implementation of both the DCT and the IDCT. The realization of both the DCT and the IDCT can rely on the same convolution module.

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